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	APPLICANT KAMIYAMA et al.	
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**U.S. Patent Documents**

Examiner Initial	DOCUMENT NUMBER	DATE	NAME	CLA SS	SUBC CLASS	FILING DATE

**Foreign Patent Documents**

Examiner Initial	DOCUMENT NUMBER	FILING DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION	
						YES	NO

**Other Documents (Including Author, Title, Date Pertinent Pages, Etc.)**

TL	Manzur Gill, Tyler Lowrey and John Park, "Ovonic Unified Memory - A High-Performance Nonvolatile Memory Technology for Stand-Alone Memory and Embedded Applications", 2002 IEEE International Solid-State Circuits Conference, Digest of Technical Papers, 3 pages
TL	A. Pirovano, A.L. Lacaita, D. Merlani, A. Benvenuti, F. Pellizzer and R. Bez, "Electronic Switching Effect in Phase-Change Memory Cells", 2002 IEEE International Electron Devices Meeting, Technical Digest, pp. 923-926
TL	Y.N. Hwang et al., "Completely CMOS-Compatible Phase-Change Nonvolatile RAM Using NMOS Cell Transistors", 2003 Non-Volatile Semiconductor Memory Workshop, Digest of Technical Papers, pp. 91-92

EXAMINER <i>Thoyte</i>	DATE CONSIDERED 6/18/05
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\*This abstract corresponds to Japanese Patent 3127220 above

\*\*The English abstract of this document is Japanese Publication 10-246764 above

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